

# *N*-Phase Synchronization of Asymmetric Attractors in a Ring of Coupled Chaotic Circuits

Takuya Nishimoto\*, Yoko Uwate\*, Yasuteru Hosokawa†, Yoshifumi Nishio\* and Danièle Fournier-Prunaret‡

\*Dept. of Electrical and Electronic Eng., Tokushima University,

2-1 Minami-Josanjima, Tokushima, 770-8506 Japan

Email: {nishimoto, uwate, nishio}@ee.tokushima-u.ac.jp

†Dept. of Media and Information Systems, Shikoku University,

Furukawa, Ohjin, Tokushima, 771-1192, Japan

Email: hosokawa@keiei.shikoku-u.ac.jp

‡LAAS-CNRS, 7 avenue du colonel Roche, F-31077 Toulouse Cedex 4, France

Email: danièle.fournier@insa-toulouse.fr

**Abstract**—Chaos synchronization can be observed in various natural systems. In this study, we investigate synchronization phenomena on a ring of coupled chaotic circuits. We can observe *N*-phase chaos synchronization and simple synchronization phenomena depending on initial values in the system. This paper suggests ring topology and alternately shifted bias are essential factors of *N*-phase synchronization.

## I. INTRODUCTION

Chaos synchronization observed on large-scale coupled chaotic systems have been attracted attention in various science field because it can be regarded as models of real physical system. For instance, chaos phenomena have been reported in engineering, biology, economics, astronomy and so on. Therefore the investigation of synchronization phenomena on high-order chaotic systems are very important to grasp essentials of the phenomena observed in natural system.

On the other hand, some chaotic circuits have coexisting attractors [1]. Asymmetric attractor observed in the system which is symmetric structure causes some interesting phenomena [2],[3]. However there are not many studies of synchronization on asymmetric attractor. It is considered that observation of synchronization phenomena in the state of chaos on asymmetric attractor is difficult.

In this study, we also investigate synchronization phenomena in a ring of coupled chaotic system whose subcircuits generate asymmetric attractor. 2-types synchronization phenomena depending on initial values can be stably observed in the system. Especially, *N*-phase chaos synchronization observed in the system ( $N = 6 \sim 50$ ) is extremely interesting phenomena since *N*-phase synchronization can be observed in small-scale coupled chaotic system normally. In this study, we confirm *N*-phase synchronization phenomena on large-scale system. Moreover, similar behaviour can be observed in another system with VDP circuit which include ideal current source. By carrying out circuit experiments and computer simulations, we can confirm the phenomena above. Namely, the phenomena possibly observed in many systems which have asymmetric attractor and ring topology.

This paper is organized as follows. In Section II, we propose a ring of coupled chaotic system. In Section III, 2-

type chaos synchronization are shown. Be based on Section III, another example with VDP model are explained in Section IV. Finally, Section V concludes this paper.

## II. SYSTEM MODEL

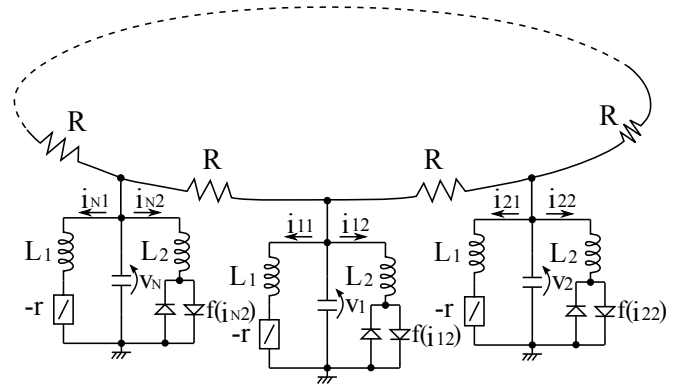


Fig. 1. System model.

The system model is shown in Fig. 1. Each subcircuit is coupled by resistor. Subcircuit is chaotic circuit which consists of three memory elements, one linear negative resistor, and bi-directionally-coupled diodes proposed by Nishio et al [5]. First, we approximate  $i-v$  characteristic of bi-directionally-coupled diodes as following function:

$$f(i_{n2}) = \frac{1}{2} (|r_d i_{n2} + V| - |r_d i_{n2} - V|) \quad (1)$$

where parameter  $r_d$  denotes slope of nonlinear resistance. Then the system equation is described by the following  $3 \times N$ -dimensional ordinary differential equation:

$$\begin{cases} L_1 \frac{di_{n1}}{dt} = v_n + r i_{n1}, \\ L_2 \frac{di_{n2}}{dt} = v_n - f(i_{n2}), \\ C \frac{dv_n}{dt} = -i_{n1} - i_{n2} + \frac{1}{R} (v_{n-1} - 2v_n + v_{n+1}). \end{cases} \quad (2)$$

By changing the parameters and variables,

$$\begin{aligned} t &= \sqrt{L_1 C} \tau, i_{n1} = V \sqrt{\frac{C}{L_1}} x_n, i_{n2} = V \sqrt{\frac{C}{L_1}} y_n, \\ v_n &= V z_n, \dots = \frac{d}{d\tau}, \alpha = r \sqrt{\frac{C}{L_1}}, \beta = \frac{L_1}{L_2} \\ \gamma &= r_d \sqrt{\frac{C}{L_1}} \text{ and } \delta = \frac{1}{R} \sqrt{\frac{L_1}{C}} \end{aligned} \quad (3)$$

the normalized system equation is described as follows:

$$\begin{cases} \dot{x}_n = \alpha x_n + z_n, \\ \dot{y}_n = \beta \{z_n - \hat{f}(y_n)\}, \\ \dot{z}_n = -x_n - y_n \\ \quad + \delta (z_{n-1} - 2z_n + z_{n+1}), \end{cases} \quad (4)$$

$(n = 1, 2, \dots, N)$

where

$$z_0 = z_N, \quad z_{N+1} = z_1 \text{ and}$$

$$\hat{f}(y_n) = \frac{1}{2} (|\gamma y_n + 1| - |\gamma y_n - 1|).$$

Figure 2 shows an example of asymmetric chaos attractor depending on initial value obtained from one subcircuit. For the computer simulation, we fixed the parameters as  $\beta = 3.0$  and  $\gamma = 470.0$ . Asymmetric attractors are distinguished with 2 colors in simulation result. In this study, we distinguish the attractors as follows:

- (1) A Poincaré section is defined at  $z_n = 1.0$  and  $\dot{y}_n < 0$ .
- (2) When the solution hits the Poincaré section, and  $y_n > 0.675$ , the color is set as red and such state is defined state  $\{M\}$ . In the case of  $y_n \leq 0.675$ , the color is set as blue and defined state  $\{P\}$ .

This definition is important to grasp the domain of attractor in each subcircuit and as such, is being applied to following simulation results in this paper.

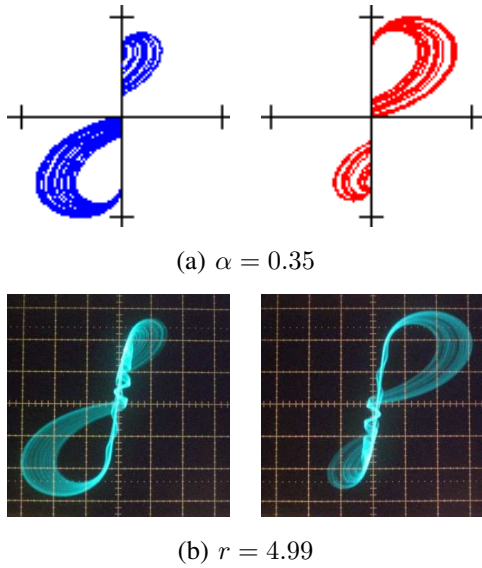


Fig. 2. Asymmetric chaos attractor observed in a subcircuit. (a) Simulation result. Horizontal:  $y_1$ . Vertical:  $z_1$ .  $\beta = 3.0$  and  $\gamma = 470.0$ . (b) Circuit experiment. Horizontal:  $i_{12}$  [0.1mA/div]. Vertical:  $v_1$  [V/div].  $L_1 = 642.8$  [mH],  $L_2 = 211.1$  [mH]  $C = 7.06$  [nF].

### III. 2-TYPE CHAOS SYNCHRONIZATION

2-type synchronization phenomena depending on initial values can be observed in the system shown in Fig. 1. Figure 3 shows the examples of synchronization phenomena.  $N$ -phase chaos synchronization is shown in Fig. 3 (a), and synchronization in Fig. 3 (b). Red and blue colors show the state  $\{M\}$  and  $\{P\}$  respectively. Both synchronization states are not synchronized rigorously since the system generates chaotic signal. However, those behaviours shown in Fig. 3 (a) and (b) are almost synchronized. Therefore these phenomena are called  $N$ -phase synchronization and synchronization respectively in this paper. It should be noted that when the subcircuits ( $n$  is odd number) take state  $\{P\}$ , while the others ( $n$  is even number) take state  $\{M\}$  in Fig. 3. Namely, the subcircuits take opposite attractor state with respect to the neighbor subcircuits. An overview of attractor domain is shown in Fig. 4. This is the one of the characteristic of this chaotic circuit in case of coupled by resistor. It is considered that shifted bias waveform be caused due to the characteristic in this system. Figure 5 is

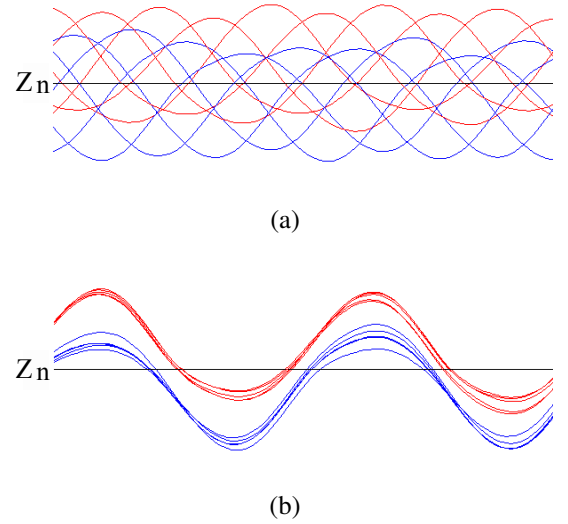


Fig. 3. 2-type synchronization phenomena depending on initial values. (a)  $N$ -phase chaos synchronization. (b) Synchronization. Vertical:  $z_n$ . Horizontal:  $\tau$ .  $N = 10$ ,  $\alpha = 0.425$ ,  $\beta = 3.0$ ,  $\gamma = 470.0$  and  $\delta = 0.20$ .

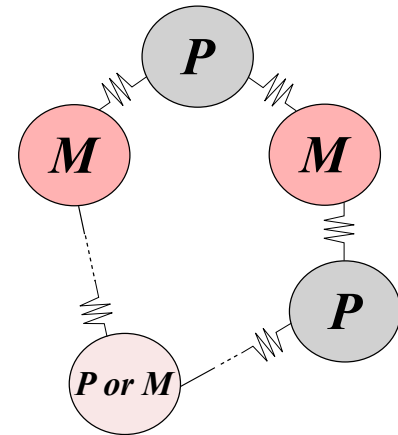


Fig. 4. Overview of attractor domain.

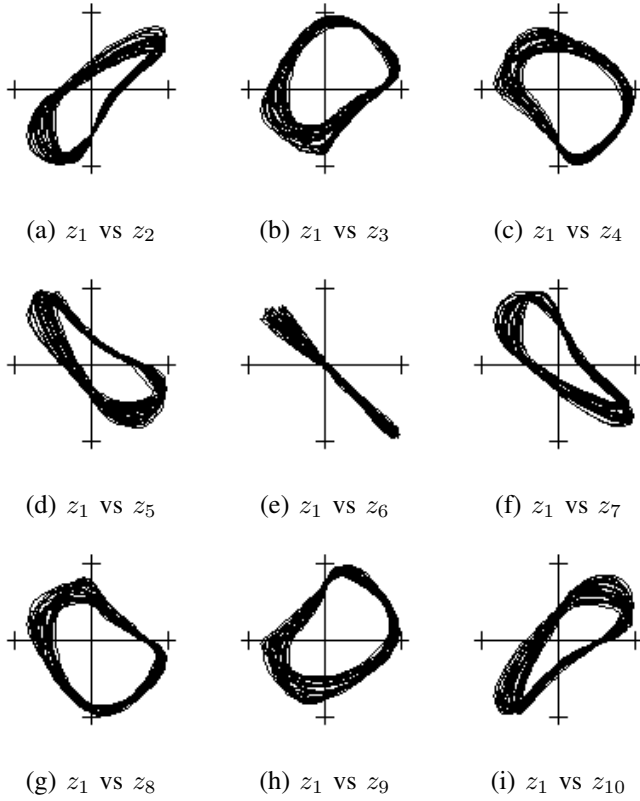


Fig. 5. Lissajous figures in state of  $N$ -phase chaos synchronization (computer simulations).  $z_1$  vs  $z_n$ .  $\alpha = 0.425$ ,  $\beta = 3.0$ ,  $\gamma = 470.0$  and  $\delta = 0.2$ .

simulation results in state of  $N$ -phase chaos synchronization. The phase difference between  $z_1$  and  $z_n$  is enlarged from  $z_1$  vs  $z_2$  to  $z_1$  vs  $z_6$ . Moreover, the subcircuits of diagonal positions are almost synchronized with anti-phase. This behaviour also can be observed in circuit experiment as shown in Fig. 6. Figure 7 shows time waveforms and Lissajous figure of  $z_1$  and  $z_6$  observed in simulation and circuit experiment. While  $z_1$  takes state  $\{P\}$ ,  $z_6$  takes state  $\{M\}$  and almost synchronized with anti-phase as shown in Fig. 7. Figure 8 shows another type synchronization. Although  $z_1$  and  $z_6$  take opposite attractor state, these circuits are almost synchronized in-phase. This behaviour can be observed by giving different initial values.

From this result, we can confirm that subcircuits which are located in diagonal position are synchronized with anti-phase or in-phase though both subcircuits take opposite attractor state. Moreover, 2-type synchronization phenomena can be observed in large-scale case around  $N = 50$ .

#### IV. ANOTHER EXAMPLE

From Section III, we found that two-type synchronization state is caused by ring topology and shifted bias waveforms. Since asymmetric attractors can be regarded as alternately shifted biases. Therefore we propose new subcircuit model with VDP circuit which includes ideal current source in order to confirm the essential factors of 2-type synchronization. The circuit model is shown in Fig. 9. Ideal current sources are used at the subcircuit located at  $n$  (odd number) with forward direction, and the subcircuit located at  $k$  (even number) with backward direction to make biases shift. We approximate the

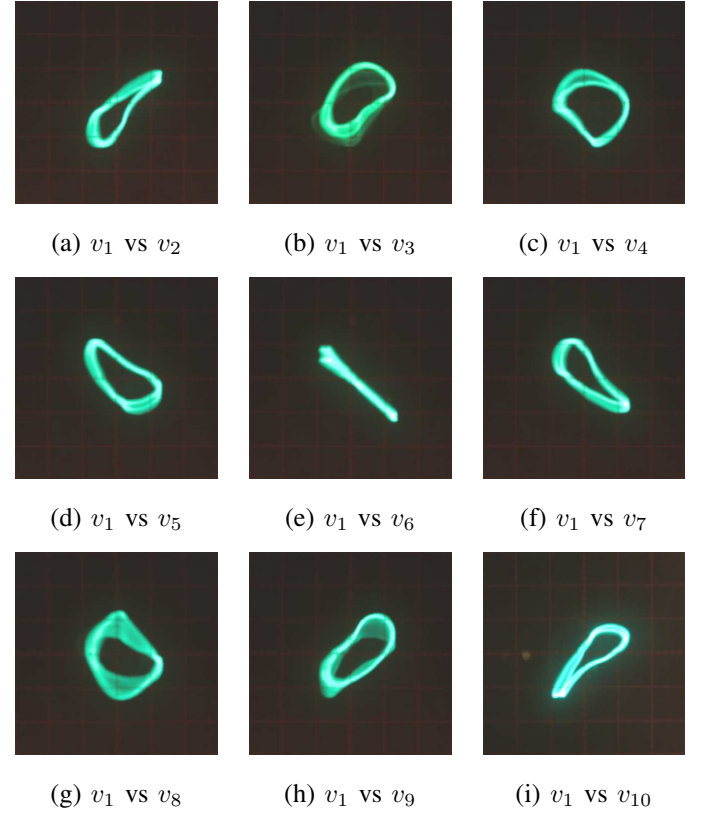


Fig. 6. Lissajous figures in state of  $N$ -phase chaos synchronization (circuit experiments).  $v_1$  vs  $v_n$  [10V/div].  $L_1 = 625[\text{mH}] \pm 1\%$ ,  $L_2 = 209[\text{mH}] \pm 1\%$ ,  $C = 1.49[\text{nF}] \pm 4\%$  and  $R = 100.0[\text{k}\Omega] \pm 1\%$ .

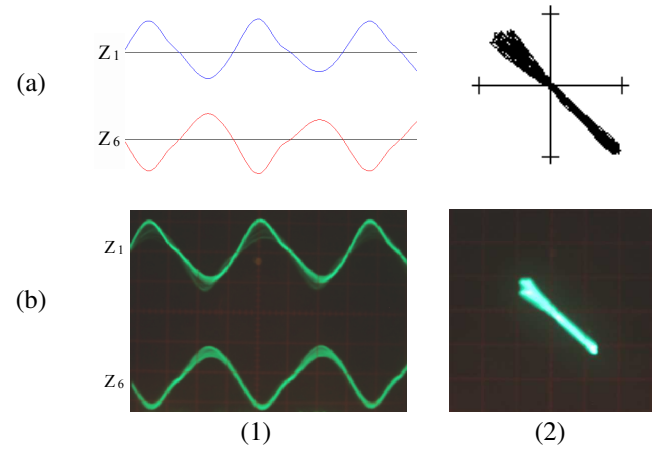


Fig. 7. Almost anti-phase synchronization state for  $N = 10$ . (a) Simulation result. (1) Time waveforms of  $z_1$  and  $z_6$ . (2)  $z_1$  vs  $z_6$ . (parameter values are the same as those in Fig. 5). (b) Circuit experiment. (1) Time waveforms of  $v_1$  and  $v_6$ . (2)  $v_1$  vs  $v_6$ . (parameter values are the same as those in Fig. 6).

$v_n - i_{Rn}$  characteristics of nonlinear resistor including ideal current source as follows:

$$i_{Rn} = -g_1 v_n + g_3 v_n^3 + (-1)^n J \quad (5)$$

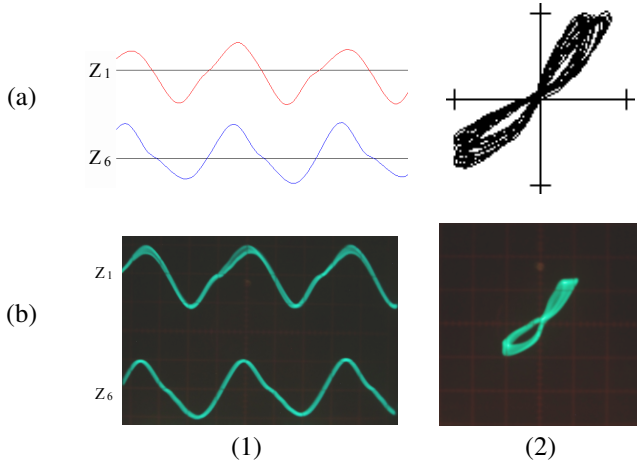


Fig. 8. Synchronization state for  $N = 10$ . (a) Simulation result. (1) Time waveforms of  $z_1$  and  $z_6$ . (2)  $z_1$  vs  $z_6$ . (parameter values are the same as those in Fig. 5). (b) Circuit experiment. (1) Time waveforms of  $v_1$  and  $v_6$ . (2)  $v_1$  vs  $v_6$ . (parameter values are the same as those in Fig. 6).

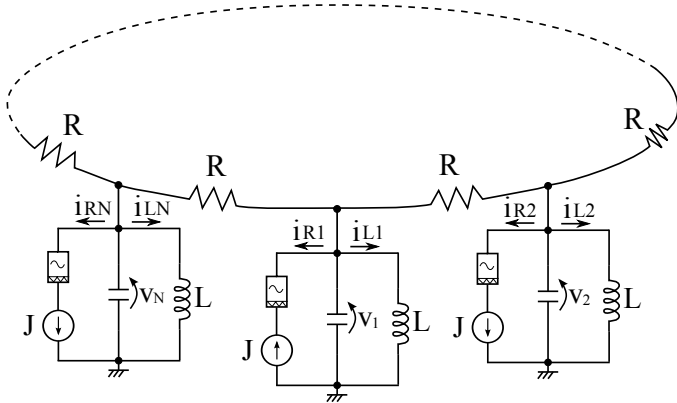


Fig. 9. System model with VDP-model.

By changing the parameters and variables,

$$\begin{aligned} t &= \sqrt{LC}\tau, \quad v_n = \sqrt{\frac{g_1}{g_3}}x_n, \quad i_{Ln} = \sqrt{\frac{g_1}{g_3}}\sqrt{\frac{C}{L}}y_n, \\ \varepsilon &= g_1\sqrt{\frac{L}{C}}, \quad \delta = \frac{1}{R}\sqrt{\frac{L}{C}} \quad \text{and} \quad \beta = \sqrt{\frac{g_3}{g_1}}\sqrt{\frac{L}{C}}J \end{aligned} \quad (6)$$

the normalized system equation is described as follows:

$$\begin{cases} \dot{x}_n = \varepsilon(1 - x_n^2)x_n - y_n + (-1)^n\beta \\ \quad + \delta(x_{n-1} - 2x_n + x_{n+1}), \\ \dot{y}_n = x_n, \end{cases} \quad (n = 1, 2, \dots, N) \quad (7)$$

where

$$x_0 = x_N \text{ and } x_{N+1} = x_1.$$

Figure 10 shows simulation results for the system shown in Fig. 9. 2-type synchronization depending on initial values also can be observed in this system. The time waveforms are described with 2 colors following condition:

- (1) Blue:  $n$  is odd.
- (2) Red:  $n$  is even.

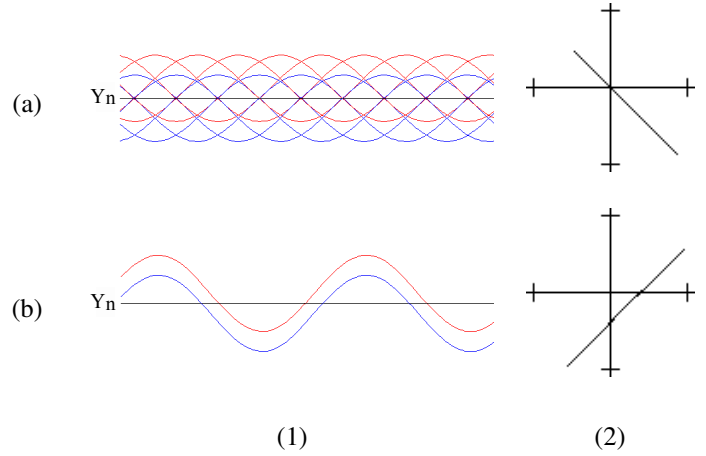


Fig. 10. 2-type synchronization phenomena observed in another system model as shown in Fig. 9. (a)  $N$ -phase synchronization state. (b) Synchronization state. (1) Time waveforms of  $y_n$ . (2)  $y_1$  vs  $y_6$ .  $N = 10$ ,  $\varepsilon = 0.08$ ,  $\beta = 0.3$  and  $\delta = 0.05$ .

In this case, each subcircuit exactly synchronized with  $N$ -phase. Moreover, the subcircuits located in diagonal position are synchronized with opposite phase shown in Fig. 10 (2). Those 2-type synchronization phenomena also can be observed around  $N = 50$ .

From these results, we specified that the essential factors of 2-type synchronization are ring topology and alternately shifted biases.

## V. CONCLUSION

In this study, we have investigated synchronization phenomena on coupled nonlinear circuit as a ring. As a result,  $N$ -phase synchronization and simple synchronization phenomena are observed in 2 system models. Moreover, it is declared that essential factors of 2-type synchronization depending on initial values are ring topology and alternately shifted bias. This result is extremely interesting and would give the effective suggestion to make clear the nonlinear phenomena in large-scale high-dimensional system. In our future work, we will investigate the mechanism of  $N$ -phase synchronization phenomena in the system with VDP model by using averaging method.

## REFERENCES

- [1] L.O. Chua, M. Komuro and T. Matsumoto, "The double scroll family", IEEE Transactions on Circuit and Systems, vol. 33, no. 11, pp. 1073-1118, Oct. 1986.
- [2] H. Sekiya, S. Mori and I. Sasase, "Synchronization of Self-Switching phenomena on Full-Coupled Chaotic Oscillators", IEICE Trans., vol. J83-A, no. 11 pp. 1264-1275, 2000.
- [3] T. Nishimoto, Y. Hosokawa and Y. Nishio, "Anti-Phase Synchronization of Switching Phenomena in Globally Coupled System of Chaotic Circuits", IEICE Technical Report, no. NLP-48 & CAS2010-32, pp. 1-4, 2010.
- [4] Y. Nishio, K. Suzuki, S. Mori and A. Ushida, "Synchronization in Mutually Coupled Chaotic Circuits", Proceedings of European Conference on Circuit Theory and Design (ECCTD'93), vol. 1, pp. 637-642, Aug. 1993.
- [5] Y. Nishio, N. Inaba, S. Mori and T. Saito, "Rigorous Analyses of Windows in a Symmetric Circuit", IEEE Transactions on Circuit and Systems, vol. 37, no. 4, pp. 473-487, Apr. 1990.