

A Frequency Domain Analysis of Electromagnetic Fields of Printed Circuit Boards

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Abstract

For designing PCBs (printed circuit boards), it is very important to find out the locations gradating strong EMF (electromagnetic fields), where the voltages will have large peak value. Although they may be found by AC analysis of Spice, it is difficult to find the resonant points and values if the quality factor Q is too large. Because we may pass over them when we choose a large step size. We need to find out both the exact peak voltages and locations on the PCB.

In this study, we propose an efficient Spice-oriented frequency domain peak detection algorithm, which is based on the HB (harmonic balance) method using Sine-Cosine circuit, where the frequency response curve is traced by the variable step size transient analysis of Spice.

1. Introduction

Nowadays, sizes of LSIs and PCBs become smaller and smaller, and the operating frequency higher and higher. In these cases, PCBs are modeled by linear LRCG large scale plane circuits as shown in Fig. 1. They can be solved by time-domain and/or frequency domain [1][2] and we concentrate on the latter technique in this study. In this case, the frequency response curve may have many sharp resonant points for high Q circuits. It is difficult to find the exact peak points.

The resonant and anti-resonant points appear at poles and zeros of driving and transfer functions, respectively. We can not find out them with analytical method when the circuit size becomes larger. They are also found by tracing the frequency response curve $|V(\omega)|$, where the resonant and anti-resonant points correspond to the highest and lowest values. In this case, when we use AC analysis of Spice simulator, we may pass over to find out all the resonant points especially for the high Q circuits. Furthermore, it is impossible to find out the exact resonant voltages. Therefore, we need to propose a new algorithm based on the HB method where the determining equation is formulated by the coupled Sine-Cosine circuits [3]-[5]. Both the resonant and anti-resonant points correspond to the solutions satisfying $f'(\omega) = 0$ on the frequency response curve, so that

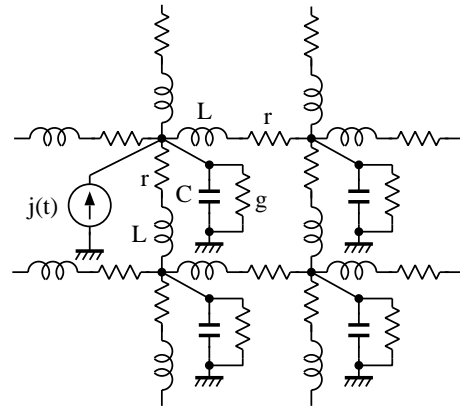


Figure 1: LRCG plane model of PCB.

we use a differentiator and a nonlinear limiter to find the $f'(\omega) = 0$ points. The circuit is solved by the variable step size transient analysis of Spice¹. Thus, we can develop an efficient simulator to trace the frequency response curves, and to find the exact peak voltages.

2. Reactance circuit

Now, we review the circuit properties of a general linear LRC circuit as shown Fig. 1. The circuit equation is given by

$$\begin{bmatrix} Y_{11} & Y_{12} & \dots & Y_{1n} \\ Y_{21} & Y_{22} & \dots & Y_{2n} \\ \vdots & \vdots & \ddots & \vdots \\ Y_{n1} & Y_{n2} & \dots & Y_{nn} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ \vdots \\ V_n \end{bmatrix} = \begin{bmatrix} J \\ 0 \\ \vdots \\ 0 \end{bmatrix} \quad (1)$$

where V_s are nodal voltages for the input J^2 , and the mutual admittance is given by

$$Y_{ij} = j\omega C_{ij} + G_{ij} + \frac{1}{j\omega L_{ij}} \quad (2)$$

¹Transient analysis of Spice is performed using the variable step size algorithm such that the step size becomes very small around rapidly changing regions.

²Although the forced input can be chosen at any nodes, we choose the node 1 without loss of generality.

Then, we have

$$V_k = \frac{\Delta_{1k} J}{\Delta} \quad (3)$$

where

$$\Delta = \begin{vmatrix} Y_{11} & Y_{12} & \dots & Y_{1n} \\ Y_{21} & Y_{22} & \dots & Y_{2n} \\ \vdots & \vdots & & \vdots \\ Y_{n1} & Y_{n2} & \dots & Y_{nn} \end{vmatrix} \quad (4)$$

Now, we consider the case $r = g = 0$ in Fig. 1 which corresponds to the reactance circuit. It is known that the voltage (3) can be written by

$$V_k(\omega) = \left[\frac{a_{0k}}{j\omega} + \sum_{i=1}^n \frac{ja_{2i,k}\omega}{\omega_{2i}^2 - \omega^2} + ja_{\infty,k}\omega \right] J \quad (5)$$

Observe that the resonant points correspond to the poles of both the driving and the transfer functions. They arise at the same frequencies. They also satisfy

$$0 < \omega_1 < \omega_2 < \omega_3 < \dots < \infty \quad (6)$$

Note that r and g in the high frequency domain are small compared to the reactance values, so that the frequency response behaves like as that of reactance circuits, and it has sharp resonant points.

3. Peak points tracing algorithm

Although, for relative low Q circuits, the AC analysis of Spice and a cubic spline combining Newton method [8] can be usefully applied to find out the peak points, it may pass over the points for high Q circuits. Thus, we propose a new algorithm based on the HB method such that the determining equation is solved by transient analysis of Spice, where ω is a function of time t as follows:

$$\omega = Kt \quad (7)$$

Now, let us discuss the Sine-Cosine circuit [3][4] corresponding to the determining equation of the HB method.

Let the current through an inductance L be

$$i_L = I_{LS} \sin \omega t + I_{LC} \cos \omega t \quad (8.1)$$

Then, the voltage v_L is given by

$$v_L = L \frac{di_L}{dt} = -\omega L I_{LC} \sin \omega t + \omega L I_{LS} \cos \omega t \quad (8.2)$$

Thus, the coefficients of $\sin \omega t$, $\cos \omega t$ are described by

$$V_{LS} = -\omega L I_{LC}, \quad V_{LC} = \omega L I_{LS} \quad (8.3)$$

Namely, the inductance is replaced by coupled current-controlled voltage sources in the Sine-Cosine transformation of the HB method. In the same way, let the voltage across a capacitor C be

$$v_C = V_{CS} \sin \omega t + V_{CC} \cos \omega t \quad (9.1)$$

Then, the current i_C is given by

$$i_C = C \frac{dv_C}{dt} = -\omega C V_{LC} \sin \omega t + \omega C V_{LS} \cos \omega t \quad (9.2)$$

Thus, the coefficients of $\sin \omega t$, $\cos \omega t$ are described by

$$I_{LS} = -\omega C V_{LC}, \quad I_{LC} = \omega C V_{LS} \quad (9.3)$$

Namely, the capacitor is replaced by the coupled voltage-controlled current sources in the Sine-Cosine transformation. The circuit corresponding to the determining equation of the HB method is driven by a constant voltage $E = J$ and ω is given by (7).

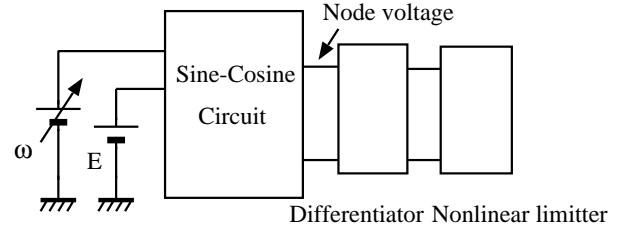


Figure 2: Peak detector of frequency response curve.

Since ω s at the peak voltages satisfy

$$\frac{d|V_k(\omega)|}{d\omega} = 0, \quad k = 1, 2, \dots, n, \quad (10)$$

on the response curve, we need to find the zero points satisfying (10)³. Hence, $|V_k(\omega)|$ need to be firstly differentiated by a differentiator. In order to detect the exact peak points, the output is limited and expanded with a nonlinear limiter as shown in Fig. 2, which consists of a limiter and nonlinear diodes. The output of the limiter is given by

$$v_L = \begin{cases} -V_{max} & : \text{for } v_{in} < -V_L \\ kv_{in} & : \text{for } -V_L \geq v_{in} \geq V_L \\ V_{max} & : \text{for } v_{in} > V_L \end{cases} \quad (11.1)$$

where

$$kV_L = V_{max} \quad (11.2)$$

In our example of section 4, we set $V_L = 2[\mu V]$, $V_{max} = 100[V]$, and $k = 0.5 \times 10^8$. The outputs of diodes are

$$i_o = \begin{cases} I_s \exp(\lambda v_o) & : \text{for } v_o > 0 \\ -I_s \exp(-\lambda v_o) & : \text{for } v_o < 0 \end{cases} \quad (11.3)$$

for $I_s = 10^{-12}$ and $\lambda = 40$.

The characteristic of the nonlinear limiter is shown in Fig. 3. This means that the regions around $d|V_k(\omega)|/d\omega = 0$ are largely expanded. Furthermore, the characteristic has large nonlinearity around the zeros. Thus, the transient analysis around the zero points is executed with a very small step size, and we can find out precise peak points.

³Note that although the peak voltages are different, the resonant frequencies arise at the same points. So, the peak detector should be attached to arbitrary selected one of the nodes in Fig. 1.

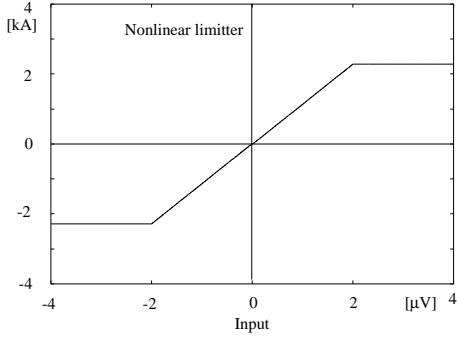


Figure 3: Input and output characteristic of nonlinear limiter.

4. Illustrative example

4.1 Theoretical analysis of transmission line

We consider a single transmission line of the length $d[mm]$, whose parameters are

$$r[\Omega/mm], L[H/mm], g[S/mm], C[F/mm].$$

The circuit equation with a complex frequency s at $x[mm]$ from the near end is given by

$$\frac{dV(x, s)}{dx} = -(r + sL)I(x, s) \quad (12.1)$$

$$\frac{dI(x, s)}{dx} = -(g + sC)V(x, s) \quad (12.2)$$

Thus, we have

$$\frac{d^2V(x, s)}{dx^2} = (r + sL)(g + sC)V(x, s) \quad (13.1)$$

$$\frac{d^2I(x, s)}{dx^2} = (r + sL)(g + sC)I(x, s) \quad (13.2)$$

Set the boundary conditions $V(0, s)$, $I(0, s)$ at the near end and $V(d, s)$, $I(d, s)$ at the far end. Then, we have

$$\begin{bmatrix} I(0, s) \\ I(d, s) \end{bmatrix} = \frac{1}{Z_0(s)} \begin{bmatrix} \coth \gamma(s)d & -\frac{1}{\sinh \gamma(s)d} \\ -\frac{1}{\sinh \gamma(s)d} & \coth \gamma(s)d \end{bmatrix} \begin{bmatrix} V(0, s) \\ V(d, s) \end{bmatrix} \quad (14)$$

where

$$Z_0(s) = \sqrt{(r + sL)/(g + sC)}$$

$$\gamma(s) = \sqrt{(r + sL)(g + sC)}$$

Thus, the poles are located at the frequencies satisfying $\sinh \gamma(s)d = 0$, namely

$$(r + sL)(g + sC) = (jn\pi/d)^2, \quad n = 1, 2, \dots \quad (15)$$

Hence,

$$p_0 = -\frac{r}{L}, \quad p_n = u_n \pm jv_n, \quad n = 1, 2, \dots \quad (16)$$

where

$$u_n = -\frac{Lg + rC}{2LC},$$

$$v_n = \frac{\sqrt{4LC(gr + (n\pi/d)^2) - (Lg + rC)^2}}{2LC}.$$

Observe that the transmission line has an infinite number of poles, where the frequency response curve has peak points.

4.2 Discrete model of transmission line

Transmission line is usually modeled by discrete RLCG ladder circuit as shown in Fig.4, where we neglected g .

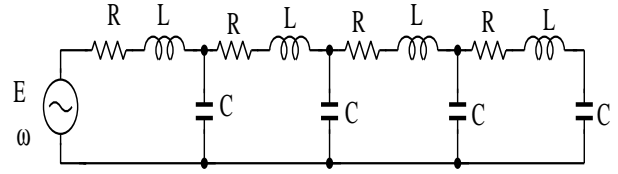


Figure 4: LRC ladder circuit $C = 1[nF]$, $L = 10[mH]$, $e(t) = E \cos \omega t$ and $E = 1[\mu V]$.

In order to obtain the frequency response curve, the circuit is transformed by HB method into the Sine-Cosine circuit. The peak detector given in section 3 is attached as shown in Fig.5. Note that the detector can be attached to an arbitrary node.

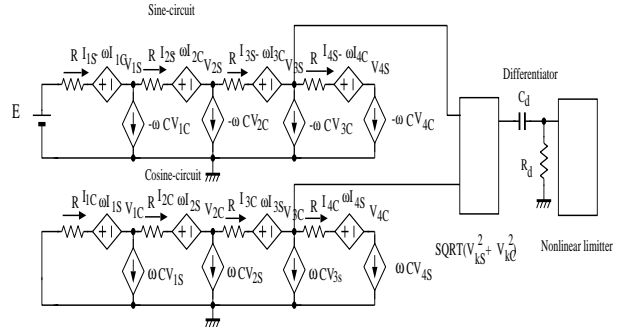


Figure 5: Sine-Cosine circuit combining with peak detector. $C_d = 1[pF]$ and $R_d = 1[\Omega]$.

Now, we show the simulation results using the transient analysis of Spice. The circuit has 4 resonant points as shown by Figs. 6(a), (b), and (c) which show the voltages at the far end of the circuit in Fig. 4, where we set the peaks 1,2,3,4 from the left hand side. The sharpnesses depend on the quality factors so that we have changed them by R_s . We found that although the resonant frequencies are almost same for all R_s as shown in Table 1, the peak values are largely different.

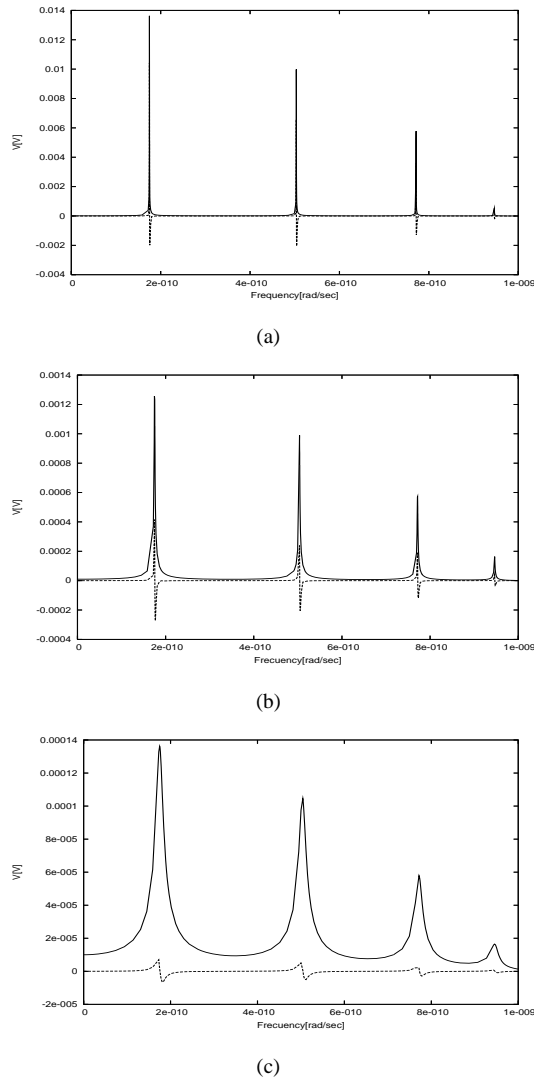


Figure 6: Frequency characteristics of the circuit in Fig. 5. (a) $R=1[\Omega]$. (b) $R=10[\Omega]$. (c) $R=100[\Omega]$.

Table 1 Calculated peak values.

$R=1[\Omega]$	peak 1	peak 2	peak 3	peak 4
ω [prad/sec]	175.1	503.7	771.6	946.7
V[mV]	13.6	10.0	5.8	0.5
$R=10[\Omega]$	peak 1	peak 2	peak 3	peak 4
ω [prad/sec]	175.5	504.2	772.1	947
V[μ V]	1256	989.5	572.5	163.7
$R=100[\Omega]$	peak 1	peak 2	peak 3	peak 4
ω [prad/sec]	175.4	504.1	772.2	944.8
V[μ V]	136.2	104.6	57.9	16.5

It seems that the calculated peak 4 for $R = 1$ and $R = 10$ is a little bit larger than the exact value. It is due to pass over the point because the peak is too narrow and small to detect even when the nonlinear limiter is introduced. In this

case, we need to trace the resonant curve around the peak 4 with a selection of much smaller initial step size.

5. Conclusions and remarks

In this study, we have proposed an algorithm to find the peaks of the frequency response curve by combining the Sine-Cosine circuit based on the HB method. The circuit is traced by the transient analysis of Spice. Since the peak points correspond to the gradient being to zeros, we differentiate the curve and find its zero points. In order to find the exact zero points, we have developed the peak detector using nonlinear limiter such that the regions are largely expanded. Thus, the step size around zero values becomes small when we use variable step size transient analysis.

Future works

1. Although we use a diode circuit for developing the nonlinear limiter, we need to find much more efficient circuit.
2. We need to apply our method to larger plane circuit as shown in Fig. 1.

References

- [1] T.Tang and M.S.Nakhla, "Analysis of high-speed VLSI interconnect using the asymptotic waveform evaluation technique," *IEEE Trans. Computer-Aided Design*, Vol.14, pp.341-352, 1992 (Japanese).
- [2] Y.Tanji, Y.Nishio and A.Ushida, "Analysis of nonuniform and nonlinear transmission lines via frequency domain techniques," *IEICE Trans. on Fundamentals*, Vol.E79-A, pp.1486-1494.
- [3] A.Ushida, Y.Yamagami and Y.Nishio, "Frequency responses of nonlinear networks using curve tracing algorithm," *Proc. of ISCAS'02*, vol.I, pp.641-644, 2002.
- [4] A.Ushida and M.Tanaka, *Simulations of Electronic Circuits*, Corona Publishing Co., Ltd, 2002.
- [5] T.Kinouchi, Y.Yamagami, Y.Nishio and A.Ushida, "Spice-oriented harmonic balance Volterra series methods," *Proc. of NOLTA'07*, pp.513-516, 2007.
- [6] Y.Nakano, *Electronic Circuit Theory*, Hirokawa publishing Co. 1976 (Japanese).
- [7] A.S.Sedra and K.C.Smith, *Microelectronic Circuits*, Oxford Univ. Press, 2004.
- [8] H.Hasegawa, et al., *Numerical Computation Methods*, Ohmsha publishing Co. 1998 (Japanese).